

Fig. 1

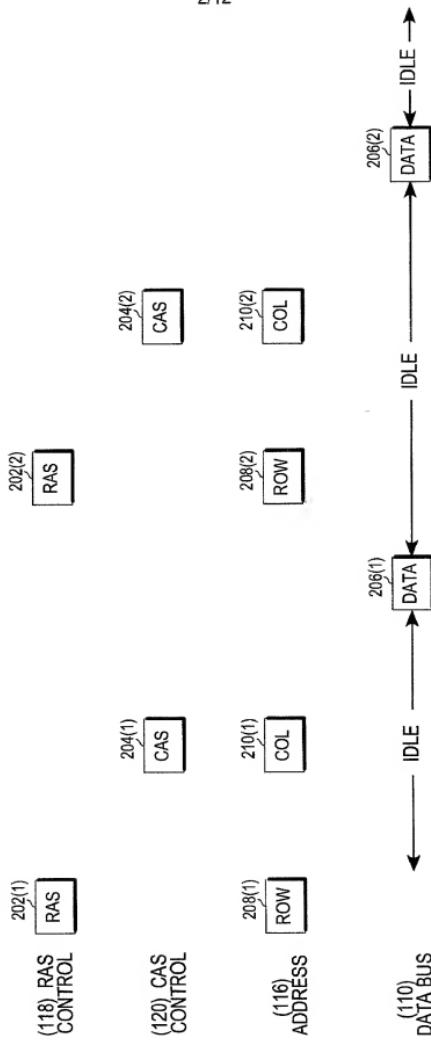


Fig. 2

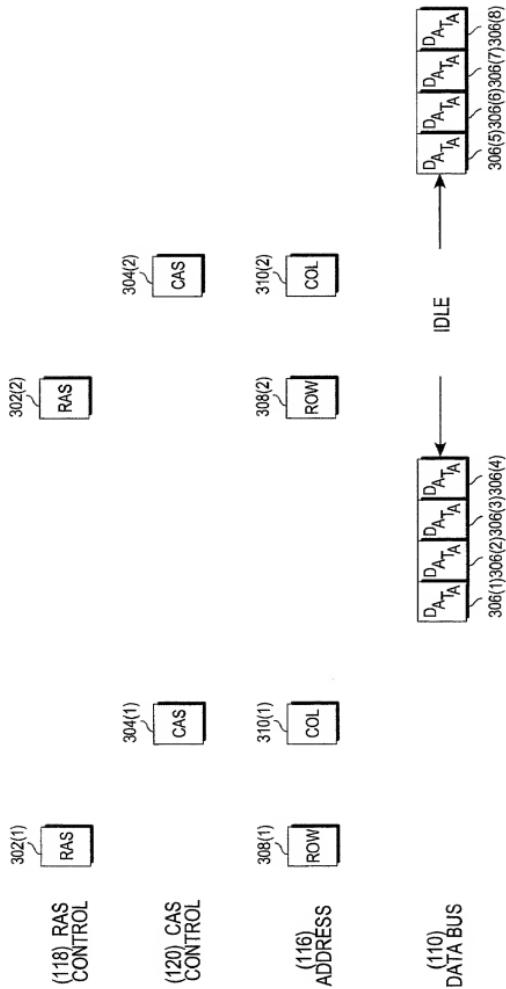


Fig. 3

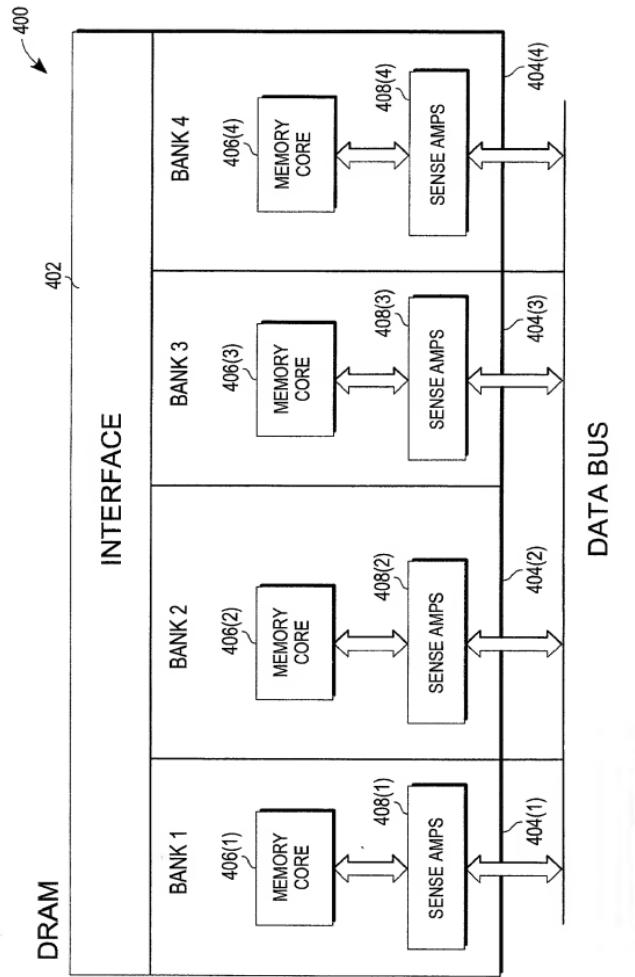


Fig. 4

+

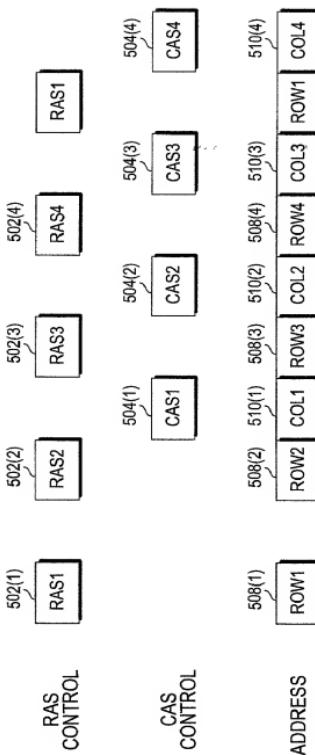


Fig. 5

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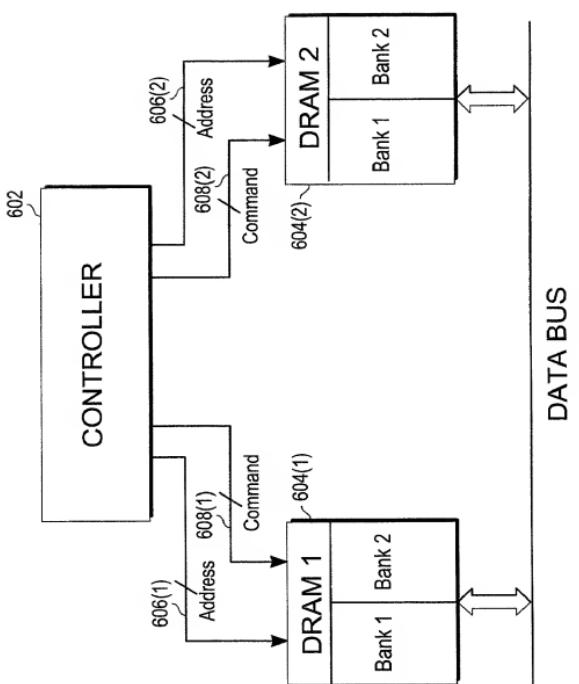
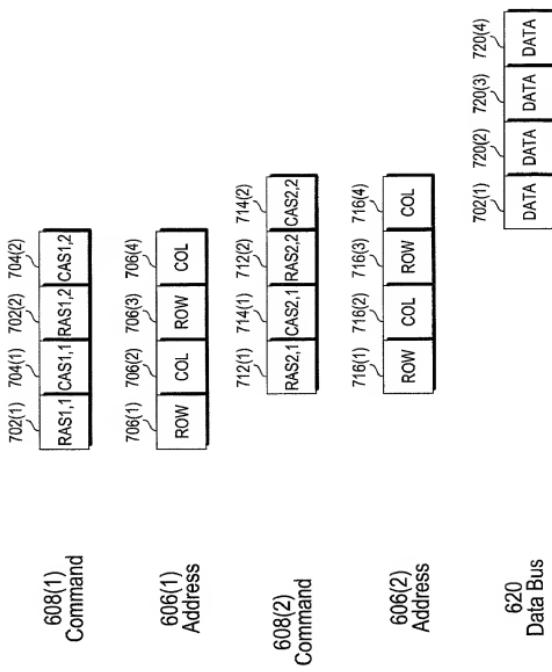


Fig. 6

Fig. 7



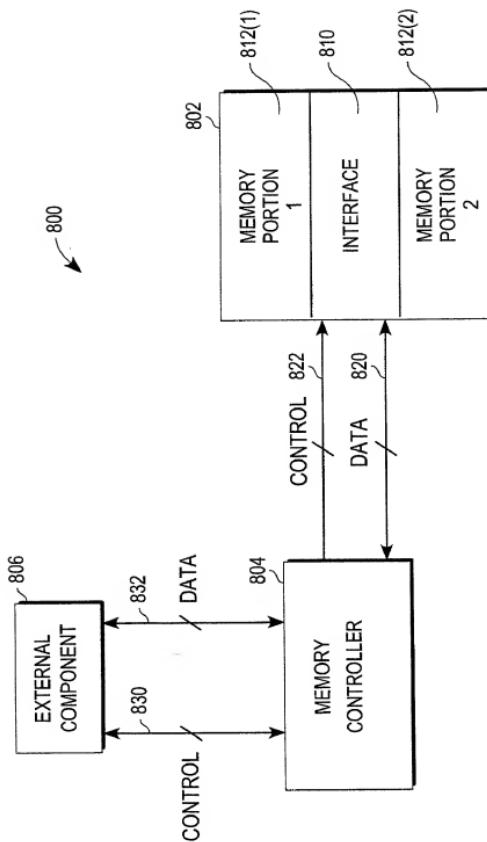


Fig. 8

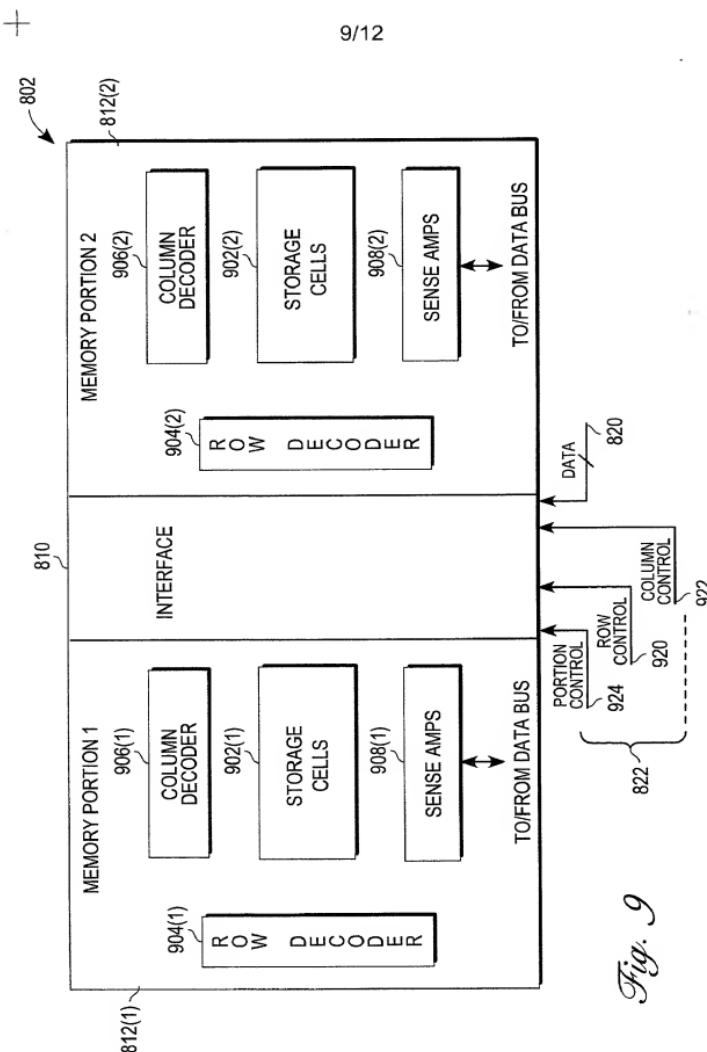


Fig. 9

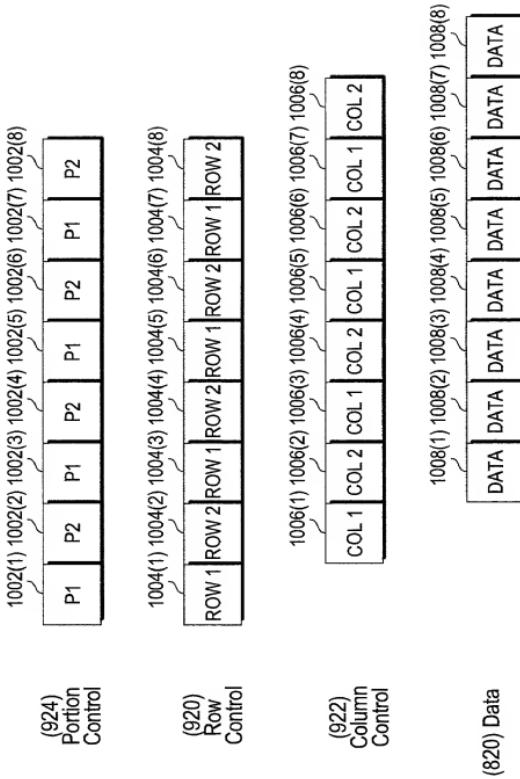


Fig. 10

图 11-2 例 11-1 的存储器示意图

PORTION
CONTROL

P1	P2	P1	P2	P1	P2	P1	P2
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CONTROL

R	O	C	R	C	R	C	R
O	W	O	O	O	O	O	O
W	1	1	2	1	2	1	2
1	2	2	1	1	2	2	1
2	1	1	2	2	1	1	2

DATA

DATA	DATA	DATA	DATA	DATA	DATA
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Fig. 11

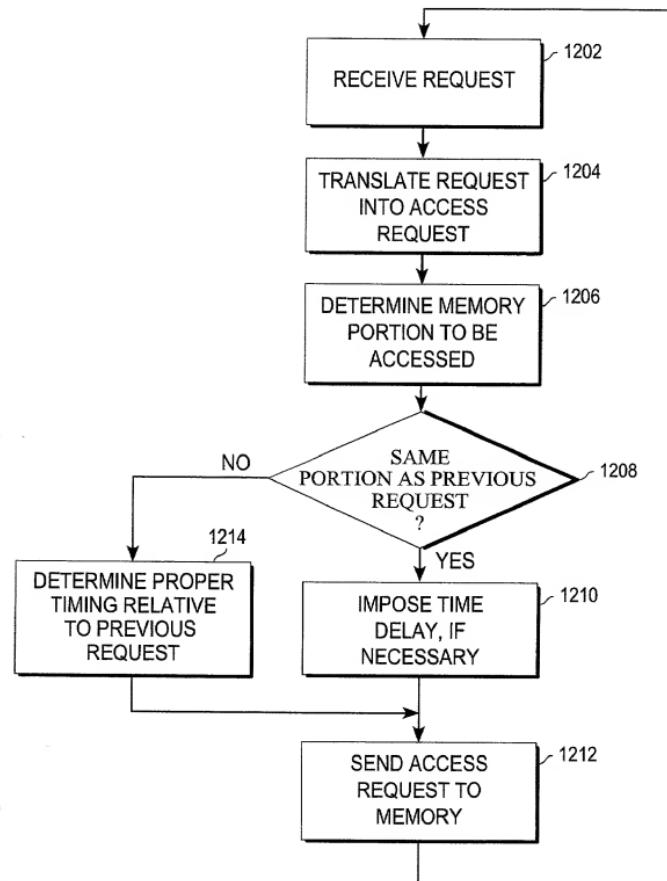


Fig. 12